

EAST

File View Edit Tools Window Help

Initia.wsp-1

BRS:

Pending

Active

- L8: (2693) (capacitive or switch\$3) adj3 (dac or (digital adj3 analog)) or cdac
- L9: (13898) reference near2 capacit\$4
- L10: (43432) (analog or input) near2 capacit\$4
- L11: (26) 9 with 10 with (integrat\$3 near3 capacit\$3)
- L12: (37787) reference near2 (sampl\$3 or capacit\$4 or sh or (sampl\$3 adj hold\$3))
- L13: (95227) (analog or input) near2 (sampl\$3 or capacit\$4 or sh or (sampl\$3 adj hold\$3))
- L14: (33216) integrat\$3 near3 capacit\$3
- L15: (39) 12 adj5 14
- L16: (133) 13 adj5 14
- L17: (0) 15 and 16 and 8
- L18: (167) 15 or 16
- L19: (5) 15 and 16
- L20: (217591) (analog or input) near2 (sampl\$3 or capacit\$4 or sh or (sampl\$3 adj hold\$3) or swi...
- L21: (57627) reference near2 (sampl\$3 or capacit\$4 or sh or (sampl\$3 adj hold\$3) or switch\$3)
- L22: (80) (20 with 21) and 8
- L23: (18908) common adj mode
- L24: (18) 22 and 23
- L25: (0) 23 near7 (sampl\$3 near4 reference) near6 (sampl\$3 near4 (analog or input))
- L26: (0) 23 with (sampl\$3 near4 reference) with (sampl\$3 near4 (analog or input))
- L27: (8) 23 with (sampl\$3 near4 reference) with (sampl\$3 near4 (analog or input))
- L28: (18352) (sum\$4 or common or mix\$3) adj2 node
- L29: (9) 28 with (sampl\$3 near4 reference) with (sampl\$3 near4 (analog or input))

Failed

DB: USPAT.USPGPUB.EPD.HF Class

Class: general OR Highlight all items marked

23 with (sampl\$3 near4 reference) with (sampl\$3 near4 (analog or input))

BRS Item Section Page Find HTML

			Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C	Inventor	S	C	P	I	D
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20030227401 A1	20031211	11	DELTA-SIGMA MODULATORS WITH IMPROVED NOISE PERFORMANCE	341/143			Yang, YuQing et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	UC
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20020039077 A1	20020404	13	Subranging sigma delta modulator	341/156	341/143		Lyden, Colin	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	UC
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6617908 B1	20030909	10	Switched-capacitor circuits with reduced distortion	327/337			Thomsen, Axel et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	UC
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6515606 B2	20030204	12	Analog to digital converter	341/143	341/144; 341/155;		Lyden, Colin	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	UC
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6323801 B1	20011127	19	Bandgap reference circuit for charge balance circuits	341/172	327/539; 341/143		McCartney, Damien et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	UC
6	<input type="checkbox"/>	<input type="checkbox"/>	US 5790064 A	19980804	10	Apparatus and method for switching capacitors within	341/172	341/155		Fujimori, Ichiro	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	UC
7	<input type="checkbox"/>	<input type="checkbox"/>	US 4755742 A	19880705	8	Dual channel time domain reflectometer	324/642	324/603; 327/91;		Agoston, Agoston et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	UC
8	<input type="checkbox"/>	<input type="checkbox"/>	US 4668936 A	19870526	16	Untrimmed 12 bit monotonic all capacitive A to D	341/172			Newcomb, Russell R. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	UC

Ready